

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

GARY P. MORRISON ET AL.

Serial No. 10/034,827 (TI-31373)

Filed January 3, 2002

For: CHIP-SCALE PACKAGES STACKED ON FOLDED INTERCONNECTOR FOR VERTICAL ASSEMBLY ON SUBSTRATES

Art Unit 2827

Examiner James M. Mitchell

Customer No. 23494

Mail Stop Appeal Brief-Patents Director of the United States Patent and Trademark Office P. O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

8-27-04

Jay M. Cantor, Reg. No. 19.906

BRIEF ON APPEAL

REAL PARTY IN INTEREST

The real party in interest is Texas Instruments Incorporated, a Delaware corporation with offices at 7839 Churchill Way, Dallas, Texas 75251.

RELATED APPEALS AND INTERFERENCES

There are no known related appeals and/or interferences.

STATUS OF CLAIMS

This is an appeal of claims 2, 4 to 10, 12, 15, 17, 18, 20 and 23, all of the rejected claims. No claims have been allowed. Please charge any costs to Deposit Account No. 20-0668.

TI-31373-1

09/21/2004 SDAVIS . 00000001 200668 .:

330.00 330.00 J